Texas Tech University. Applied Mathematics Seminar.

Toward a fault tolerant QMR

Ashley Meek, *Texas Tech University*

Wednesday, November 7, 2012

Room: MATH 014. Time: 4:00pm.

ABSTRACT. As architectures are developed with smaller components operating a lower voltages, soft errors (i.e., bit flips) become potentially worrying, and in fact may not even be detected. Previous work has investigated the sensitivity several Krylov methods to soft errors. Here, we explore fault tolerance issues in QMR, compare its performance in the presence of bit flips, and hope to introduce a fault tolerant variant of QMR.